DATA SHEET

NEC

MOS FIELD EFFECT TRANSISTOR 2SJ358

P-CHANNEL MOS FET FOR HIGH-SPEED SWITCH

The 2SJ358 is a P-channel vertical MOS FET that can be used as a switching element. The 2SJ358 can be directly driven by an IC operating at 5 V.

The 2SJ358 features a low on-resistance and excellent switching characteristics, and is suitable for applications such as actuator driver and DC/DC converter.

FEATURES

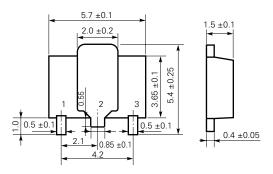
- New-type compact package Has advantages of packages for small signals and for power transistors, and compensates those disadvantages
- Can be directly driven by an IC operating at 5 V.
- Low on-resistance

RDS(ON) = 0.40 Ω MAX. @VGS = -4 V, ID = -1.5 A RDS(ON) = 0.30 Ω MAX. @VGS = -10 V, ID = -1.5 A

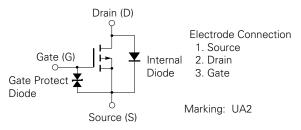
QUALITY GRADE

Standard









Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

Parameter	Symbol	Conditions	Ratings	Unit
Drain-Source Voltage	Vdss	Vgs = 0	-60	V
Gate-Source Voltage	Vgss	VDS = 0	-20/+10	V
Drain Current (DC)			-/+3.0	А
Drain Current (Pulse)	D(pulse)	PW ≤ 10 ms	-/+6.0	А
		Duty Cycle ≤ 1 %		
Total Power Loss	Pτ	Mounted on ceramic board of 7.5 $\text{cm}^2 \times 0.7~\text{mm}$	2.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

ABSOLUTE MAXIMUM RATINGS (Ta = +25 °C)

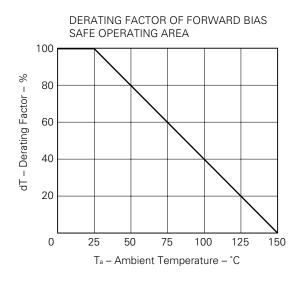
The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

The information in this document is subject to change without notice.

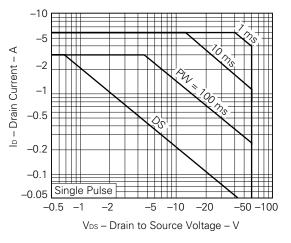
ELECTRICAL SPECIFICATIONS (Ta = +25 °C)

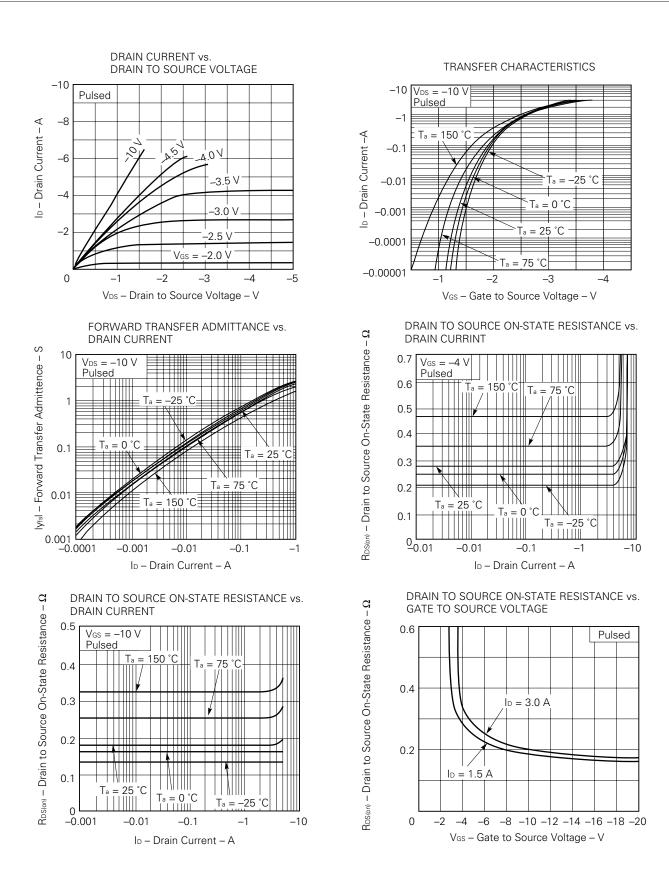
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Drain Shut-down Current	IDSS	$V_{DS} = -60 V, V_{GS} = 0$			-10	μΑ
Gate Leak Current	lgss	$V_{GS} = -16/+10 V, V_{DS} = 0$			-/+10	μΑ
Gate Cutoff Voltage	V _{GS(off)}	$V_{DS} = -10 V, I_{D} = -1 mA$	-1.0	-1.4	-2.0	V
Forward Transfer Admittance	ly _{fs} l	$V_{DS} = -10 V$, $I_{D} = -1.0 A$	1.8			S
Drain-Source On-Resistance	RDS(on)1	$V_{GS} = -4 V$, $I_D = -1.5 A$		0.29	0.40	Ω
Drain-Source On-Resistance	RDS(on)2	$V_{GS} = -10 V$, $I_D = -1.5 A$		0.18	0.30	Ω
Input Capacitance	Ciss	$V_{DS} = -10 V, V_{GS} = 0,$ f = 1.0 MHz		600		pF
Output Capacitance	Coss			300		pF
Feedback Capacitance	Crss			120		pF
On-Time Delay	td(on)	$V_{DD} = -25 \text{ V}, \text{ I}_{D} = -1.5 \text{ A}$ $V_{GS(on)} = -10 \text{ V}$ $R_{G} = 10 \Omega, R_{L} = 17 \Omega$		6		ns
Rise Time	tr			35		ns
Off-Time Delay	td(off)			155		ns
Fall Time	tr			95		ns
Gate Input Charge	QG	V _{DS} = -48 V, V _{GS} = -10 V, I _D = -3.1 A, I _G = -2 mA		23.9		nC
Gate-Source Chanrge	Q _{GS}			1.5		nC
Gate-Drain Charge	Qgd			8.1		nC
Internal Diode Reverse Recovery Time	trr	l⊧ = 3.0 A di/dt = 50 A/μs		95		ns
Internal Diode Reverse Recovery Charge	Qrr			118		nC

CHARACTERISTICS CURVES (Ta = +25 °C)

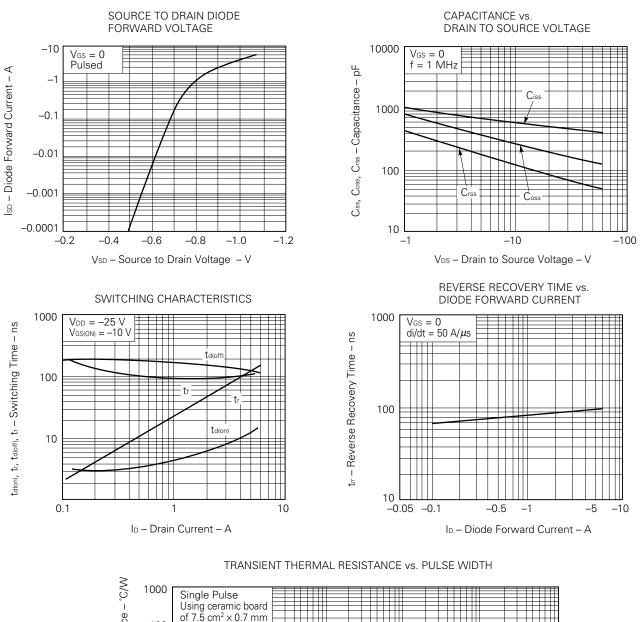


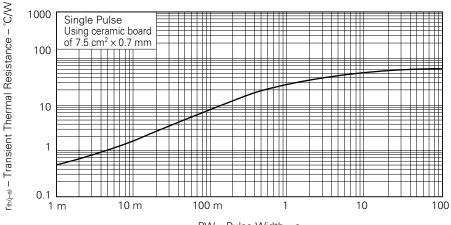
FORWARD BIAS SAFE OPERATING AREA





NEC





RELATED DOCUMENTS

Document Name	Document No.		
Semiconductor Device Mounting Technology Manual	IEI-1207		
NEC Semiconductor Device Reliability/Quality Control System	TEI-1202		
Guide to Quality Assurance for Semiconductor Device	MEI-1202		

2SJ358

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Application examples recommended by NEC Corporation

Standard: Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tools, Industrial robots, Audio and Visual equipment, Other consumer products, etc.

Special: Automotive and Transportation equipment, Traffic control systems, Antidisaster systems, Anticrime systems, etc.